



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: To Be Assigned

Re PATENT APPLICATION Of:

Applicants : Mitsuru KOMIYAMA and
Shinsuke SUZUKI

Serial No. : 09/963,590

Filed : September 27, 2001

For : MULTI-CHIP PACKAGE TYPE
SEMICONDUCTOR DEVICE

Attorney Ref. : F00ED0023

Assistant Commissioner for Patents
Washington, D.C. 20231

REQUEST FOR
CORRECTED
FILING RECEIPT

February 8, 2002

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Sir:

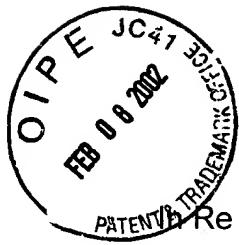
Submitted herewith is a red-marked copy of the Filing Receipt for the above-referenced application. Upon review, it has been determined there is an error in the date of Foreign Application on the declaration filed on December 20, 2001. The correct date of Foreign Application is 09/28/2000. As a substitute declaration is filed together with this request, please correct the filing receipt and send the corrected filing receipt to us.

Should any fee be required, please charge the same to our Deposit Account No. 50-0945 and advise us accordingly.

Respectfully submitted,

Junichi Mimura (Registration No. 40,351)
Oki America, Inc.
1101 14th Street, N.W., Suite 555
Washington, D.C. 20005
Telephone : (202) 452-6190
Telefax : (202) 452-6148
Customer No.: 26071

Receipt
#10
Declaration
J.M.Mimura
9/3/02



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**SUBMISSION
OF SUBSTITUTE
DECLARATION**

February 8, 2002

Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith is a Substitute Declaration for the above-referenced application, indicating that the prior Japanese application for the above-referenced application was filed on September 28, 2000.

It is respectfully requested that receipt of this Declaration be acknowledged.

Should any fee be required, please charge the same to our Deposit Account No. 50-0945 and advise us accordingly.

Respectfully submitted,


Junichi Mimura (Registration No. 40,351)
Oki America, Inc.
1101 14th Street, N.W., Suite 555
Washington, D.C. 20005
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UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
FEB 08 2002 109/963,590	09/27/2001	2814	840	F00ED0023	7	18	3

O I P E
PATENT & TRADEMARK OFFICE
20071
JUNICHI MIMURA
OKI AMERICA INC.
1101 14TH STREET, N.W.
WASHINGTON, DC 20005

CONFIRMATION NO. 9688
UPDATED FILING RECEIPT


OC000000007303581

Date Mailed: 01/14/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

 Mitsuru Komiya, Tokyo, JAPAN;
 Shinsuke Suzuki, Miyazaki, JAPAN;

Domestic Priority data as claimed by applicant
Foreign Applications

JAPAN 2000-296328 09/29/2000

→ 28

If Required, Foreign Filing License Granted 11/01/2001
Projected Publication Date: 04/25/2002
Non-Publication Request: No
Early Publication Request: No

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Title

Multi-chip package type semiconductor device

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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